Nanomaterials CNTs and Applications

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Nanomaterials

- Growing, Touching and Observing
- For lab #7
 - CNT growth
 - Surface drop test

How small is small?

- Atomic radius of silicon = 0.1 nm
- Please see http://images.books24x7.com/bookimages/ id_19474/fig62_01.jpg.

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- Size of one unit cell of silicon = 0.542 nm
- Atomic radius of carbon = 0.07 nm
- Size of one unit cell of diamond = 0.357 nm
- Thickness of hair/paper = 100 um \rightarrow 10⁵ nm

 \rightarrow around million atoms



Size of transistors in your computer \rightarrow 14 nm \rightarrow In 14x14 nm² channel: 4900 atoms

Semiconductor Materials



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Emerging nanomaterials (Low-dimensional materials)





Peter Allen, UCSB Appl. Phys. Lett. 100, 143108 (2012);



Intech "Lithography", Michael Wang ISBN 978-953-307-064-3, p264

- 2D: Single-atom thickness films
 - Flexible electronics
 - Sensors
- 1D: Nanowires
 - Quantum electronics
 - Biosensors
 - Solar cell, photodetector
 - 0D: Quantum dots
 - Single electron transistor

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Why small?? Quantum confinement, high surface area, Flexibility

Carbon-based nanomaterials



History of nanomaterials

- 1959: Richard Feynman's famed talk." There's Plenty of Room at the Bottom "
- 1981: Binnig and Rohrer created the STM to image individual atoms. (Nobel, Physics 1986)
- 1985: Curl, Kroto, Smalley discovered fullerene (Nobel, Physics 1996)
- 1991: Iijima discovered single wall carbon nanotubes.
- 2010 A. Geim and K. Novoselov (Nobel physics on Graphene)

Graphene overall orbital structure



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Properties of graphene

	Graphene	Si
Electrical conductivity	~100,000 cm²/VS	450 cm2/Vs
Thermal conductivity	~5000W/K.m	1.3 W/K.m
Young's modulus	1 TPa	130~170 GPa
Transparency	0	Х
Flexibility	0	Х

The way of rolling up graphene to form CNT

Diameter determines band gap

Chirality determines semiconductor or metal This image has been removed due to copyright restrictions. Armchair Please see http://www.nanotech-now.com/images/SWNT-rollup.jpg. Zigzag Chiral Zigzag (2,0) (3,0) (4,0) (5,0) (6,0) (7,0) (6,0) (9,0) (10,0) (11,0) (1,0) (1,1)(2,2) (3,2) (4,2) (5,2) (6,2) (7,2) (8,2) (9,2) (10.2) (3,3) (4,3) (5,3) (6,3) (7,3) (8,3) (4,4) (6,4) (6,4) (7,4) 1 (8 4) (5,5) a termediate (7,7) a Armchair niconductine Setallic © AIP Publishing LLC. All rights reserved. This content is excluded from our Creative Commons license. For more

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Possible Chiral Vectors





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M. S. Dresselhaus, Electronic Structure of Chiral Graphene Tubules, Appl. Phys. Lett. 60 (18), 1992

Properties of Carbon Nanotubes

	CNT	Graphene	Si
Electrical conductivity	~100,000 cm ² /VS	>100,000 cm ² /VS	450 cm2/Vs
Thermal conductivity	~5000W/K.m	~5000W/K.m	1.3 W/K.m
Young's modulus	0.9 ~1.1TPa	1 TPa	130~170 GPa
Transparency	0	0	Х
Flexibility	0	0	Х
Band gap	Semiconductor & Metal	Semi-metal	Semicondu ctor

Application of CNT: Electronics (CNT forest)



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Volder et al. Science, 339, 535 (2013) 13

Application of CNT: Energy storage (CNT forest)



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Volder et al. Science, 339, 535 (2013) 14

Application of CNT: Solar cell electrode (CNT network)



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Application of CNTs: Atomic Force Microscope tips

Reduced diameter – maximum atomic imaging resolution (Lab 10)



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Nature Nanotechnology, 4, 483 (2009)

Application of CNT: Superhydrophobic surface (CNT forest)



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Nano Letters, 2003, 3 (12), pp 1701-1705

Application of CNTs: Electronics (Single CNT)



Nano Lett., Vol. 4, No. 1, 2004

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Chemical vapour deposition (CVD)

Fe (1 r	m) cap Al ₂ O ₃	(10 nm)	
SiO ₂ (over 100 nm)			
Si substrate			

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http://nt13.aalto.fi/docs/NT13_TutorialB.pdf, Christophe Bichara

CNT Forest formed by metal nanosphere catalyst



Longest CNTs grown?

Class award

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H.W. Lee, S. Kim, and S.G. Kim, App. Phys., 2009



(H.W. Lee, S. Kim, and S.G. Kim, MIT)

Understanding CNT growth : Formation of metal nanoparticles





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J. Kim et al, ACS Nano, 2010

Metal cannot completely wet Al_2O_3 \rightarrow Discontinuous metal islands are automatically formed at ultrathin thickness

Understanding CNT growth

: Catalytic reaction with metal particles

Catalytic metals: Fe, Ni, Co

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This image has been removed due to copyright restrictions. Please see http://www.nanobliss.com/departments/techniq ues/techniqueimages/basegrowth_nanobliss_350wide.jpg.

http://nt13.aalto.fi/docs/NT13_TutorialB.pdf Christophe Bichara

Growth modes



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- SWNT, single-walled nanotube (0.3 < d < 3 nm)
- MWNT, multi-walled nanotube (d > 10 nm)

Carbon Nanotube Synthesis and Growth Mechanism By Mukul Kumar (intechopen.com)

Understanding CNT growth : Role of catalytic metals & Al₂O₃

Role of catalytic metals

http://nt13.aalto.fi/docs/NT13_TutorialB.pdf,

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CNT growth sequence

- C dissolution into the catalyst
- \rightarrow C supersaturation
- \rightarrow C precipitation on catalytic nanoparticles
- \rightarrow CNT growth from the periphery of nanoparticles



Al2O3 enhances CNT growth

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Journal of Nanoscience and Nanotechnology, 8, (2008) 6123

In-situ observation of CNT growth

• In-situ TEM



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https://www.youtube.com/watch?v=TaNCWcumeyg

Pigos et al. ACS Nano, 5, 12, 10096-10101 (2011)

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Challenges for Carbon Nanotube Applications

- Process control to produce nanotubes with same diameter and chirality.
 - **Purification**/sorting methods required for uniform CNT
 - Placement/alignment methods required for long-range order
- Develop large-scale, high productivity synthesis methods.
- Develop large-scale, long range order assembly processes deterministically.

- ASSEMBLY, ASSEMBLY, ASSEMBLY!!!
- Graphene \rightarrow Lab 11

Placement: Key issue to realized benefit of CNT



Sub-10 nm CNTFET, IBM, 2012

(Franklin et. al., Nano Lett. 12, 758)

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Requirement:

- High density of individual CNTs (transistor density x CNTs/transistor ~ 10¹⁰/cm²)
- Alignment with a constant pitch (< 10 nm)
- Compatibility with wafer-scale CMOS process
- Compatibility with a process for high purity of semiconducting CNTs

Aligned growth of CNTs

Lateral growth of CNT at the step edges of sapphire wafers



J. AM. CHEM. SOC. 2005, 127, 11554

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Conventional placement options



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Challenges:

- Throughput of scanning probe techniques
- Demonstrated on Au substrates

Challenges:

- Biasing to billions of transistors
- Scaling: limitation of minimum pad size
- Density: interference between electrodes

Specific Surface Functionalization



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- Strong electrostatic interaction between CNTs and surface monolayer
- Surface monolayer (NMPI): self-assembled on $HfO_2 \rightarrow \underline{Positively}$ charged
- CNTs: dispersed in a normal surfactant solution (1% SDS) → <u>Negatively</u> charged H. Park *et al.*, *Nature Nanotechnology* 7, 787(2012)

NMPI: 4-(N-hydroxycarboxamido)-1-methylpyridinium iodide

Position control: excellent selectivity and high density



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Ion-exchange chemistry enables high density and potential for scaling

- Coulombic bonding → high density in small dimensions
- H. Park et al., Nature Nanotechnology 7, 787(2012)

Record density: 10⁹/cm² ³¹

The New York Times

I.B.M. Reports Nanotube Chip Breakthrough

By JOHN MARKOFF

OCTOBER 28, 2012 2:00 PM

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are reporting progress in a chipmaking technology that is likely to ensure that the basic digital switch at the heart of modern microchips will continue to shrink for more than a decade.

SAN FRANCISCO – I.B.M. scientists



The face of an I.B.M. research scientist, Hongsik Park, is reflected in a wafer used to make microprocessors. I.B.M. Research

The advance, first described in the journal Nature Nanotechnology on



Sunday, is based on carbon nanotubes — exotic molecules that have long held out promise as an alternative to silicon from which to create the tiny logic gates now used by the billions to create microprocessors and memory chips.

The I.B.M. scientists at the T.J. Watson Research Center in Yorktown Heights, N.Y., have been able to pattern an array of carbon nanotubes on the surface of a silicon wafer and use them to build hybrid chips with more than 10,000 working transistors.

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CNT vs Graphene

	CNT	Graphene	Si
Electrical conductivity	~100,000 cm ² /VS	~100,000 cm²/VS	450 cm2/Vs
Thermal conductivity	~5000W/K.m	~5000W/K.m	1.3 W/K.m
Young's modulus	0.9 ~1.1TPa	1 TPa	130~170 GPa
Transparency	0	0	X
Flexibility	0	0	х
Band gap	Semiconductor	Semi-metal	Semiconductor
Scalability	X	0	

Why graphene?



Flat/Monolayer/Single-crystalline Uniform in a LARGE SCALE

First single-crystalline wafer-scale graphene



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Jeehwan Kim *et al. Science*, 342, 833 (2013)

Application of CNT: Superhydrophobic surface (CNT forest)



Nano Letters, 2003, 3 (12), pp 1701–1705

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Wenzel's model

- If the surface has a high free energy, roughness promotes wetting.
- If it has low free energy, roughness promotes hydrophobicity.

 $\cos \theta^* = r \cos \theta$ $r = \frac{actual_area}{projected_area}$ $\theta^* = apparent_contact_angle$

Cassie's model

- Wettability of heterogeneous (solid+air) surfaces
- Contact angle on air fraction is 180⁰.

 $\cos \theta^* = -1 + \phi_s (\cos \theta + 1)$ $\phi_s = solid_fraction_surface$

Bouncing a water drop



rebounding

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When kinetic energy is very high



Restitution ratio=|v'/v| = Relative speed after collision/ Relative speed before collision/

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We = ρV ²R/γ= kinetic energy/surface energy → Bouncing patterns determined by Weber number Low We: No deformation/Mid We: Deformation/High We: Break off

Lotus Effect



- Some plant leaves have near 170^o contact angle, and show no accumulation of dirt. (Lotus Effect)
- Superhydrophobicity by nano patterned surface
- Self-cleaning surface (no car wash?)

W. Barthlott and C. Neinhuis, Planta 202, 1 (1997)



Nanotech Lecture: 'Self-Cleaning Surfaces' by Dr. Vesselin Paunov

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Bouncing a milk drop

H. Doc Edgerton, MIT

















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