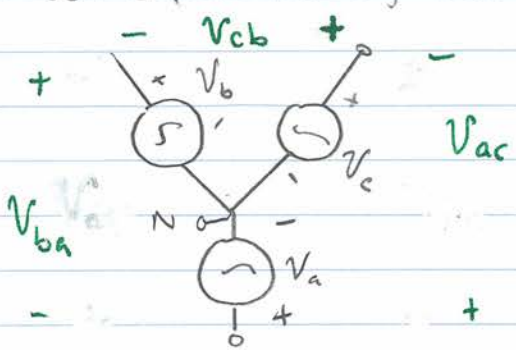


REVIEW: Three-phase set separated by 120°

$$V_a = \sqrt{3} V_s \sin(\omega t)$$

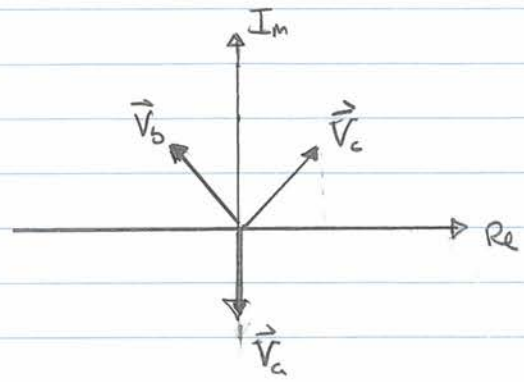
$$V_b = \sqrt{3} V_s \sin(\omega t - \frac{2\pi}{3})$$

$$V_c = \sqrt{3} V_s \sin(\omega t + \frac{2\pi}{3})$$

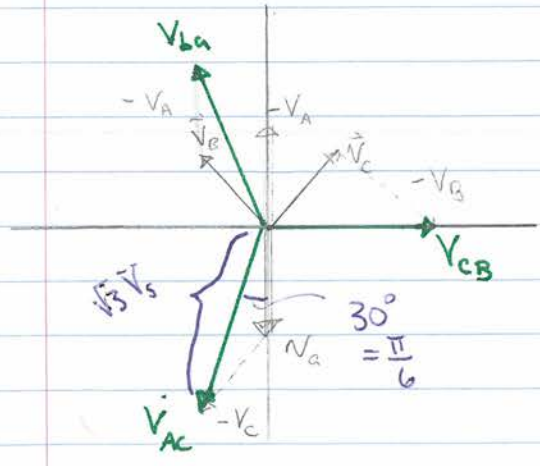


Phasor Notation:

$$v_x(t) = \text{Re} \left\{ \vec{V}_x e^{j\omega t} \right\}$$



WE CAN TAKE VECTOR DIFFERENCES TO GET line-line waveforms



- $\sqrt{3}$  increase in magnitude for l-l
- 30° phase shift for l-l.

So:  $120 V_{rms} \text{ l-n} \leftrightarrow 208 V_{rms} \text{ l-l.}$

⇒ Distribute 3φ 208 l-l and break off l-n taps for different 1φ branches

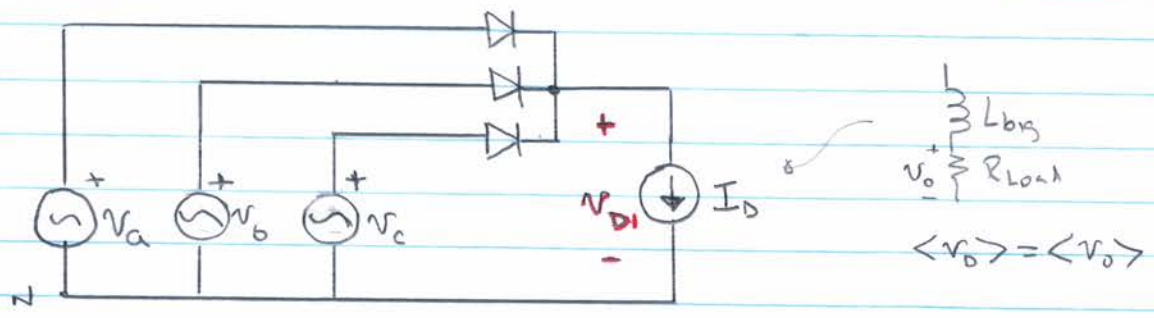
We can create sinusoids of any magnitude + phase by using transformers to select + add up parts of different l-n voltages! (as simple as vector addition....)

6.334 Lecture Notes

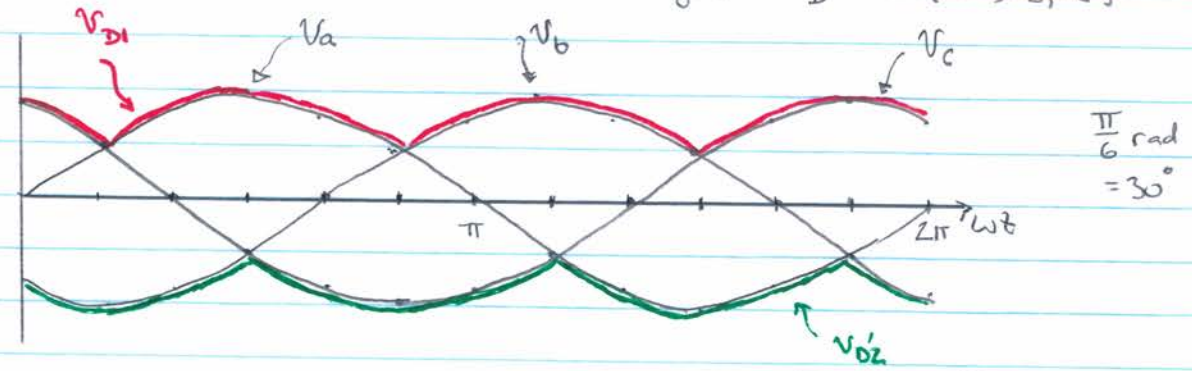
3φ Systems #2

Consider diode rectification of 3-phase

The equivalent of "half-wave" rectification is:

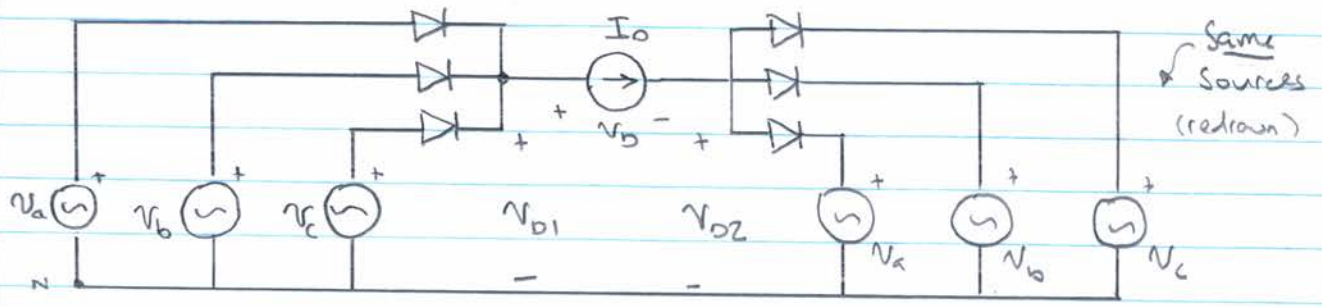


$v_D$  is the diode "or" of the three <sup>line-to-neutral</sup> voltages  $v_D = \max\{v_a, v_b, v_c\}$



If we had reversed the diodes, we'd get  $v_{D2} = \min\{v_a, v_b, v_c\}$

For "full wave" or "bridge" rectification, use 6 diodes for max + min:



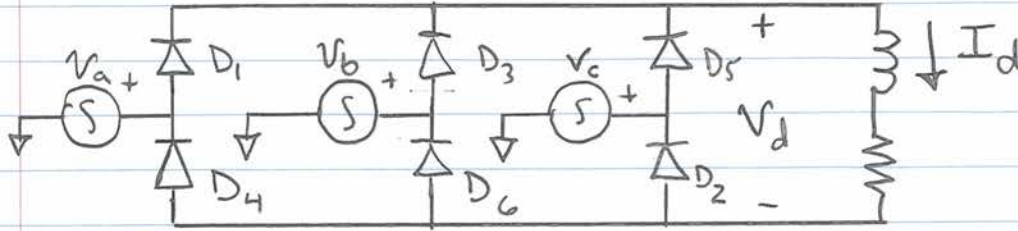
$$v_D = v_{D1} - v_{D2} = \max\{v_a, v_b, v_c\} - \min\{v_a, v_b, v_c\}$$

\* This is the same as  $v_D = \max\{v_{ab}, v_{ac}, v_{bc}, v_{ba}, v_{ca}, v_{cb}\}$

G.334 Lecture Notes

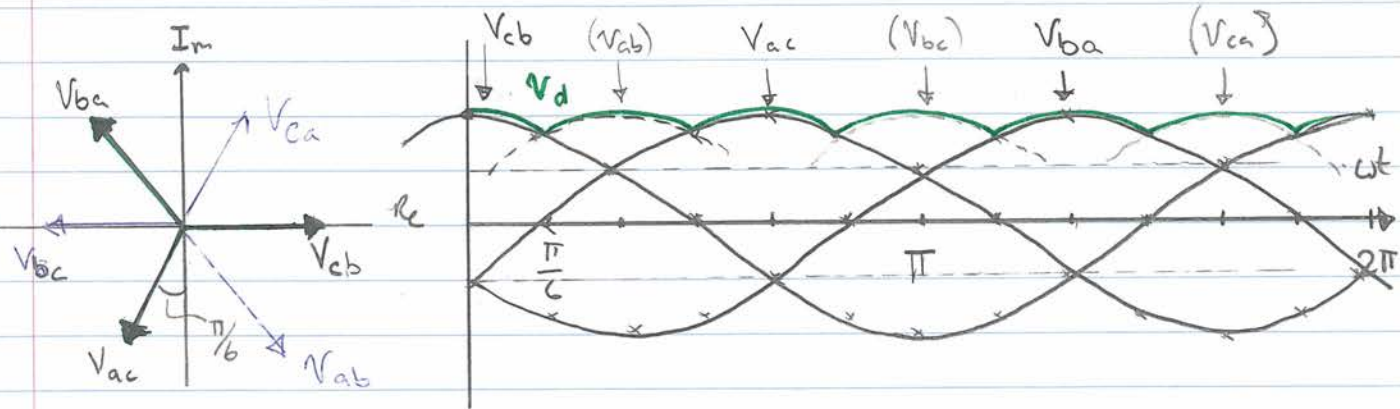
3φ systems # 2

The 3φ Full-bridge rectifier can be drawn as follows:



Note: 1φ rect 4 diodes  
3φ rect 6 diodes!

Note: 1φ Vd  
3φ Vd  
Smaller filter!

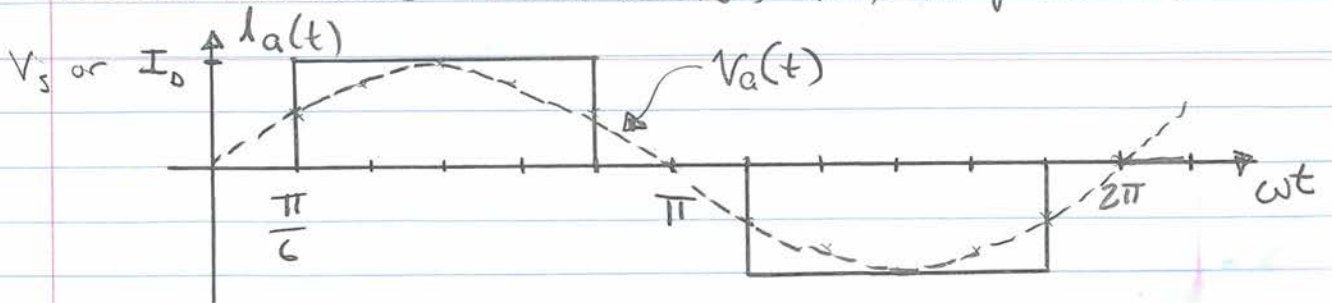


$V_d$  is  $\max \{ V_{cb}, V_{ac}, V_{ba}, V_{ab}, V_{bc}, V_{ca} \}$

TO SIMPLIFY TRACKING BEHAVIOR:

- Phases named such that successive l-n maxime are  $V_a, V_b, V_c, V_a, V_b, V_c, \dots$   
so l-l max goes  $V_{ab}, V_{ac}, V_{bc}, V_{ba}, V_{ca}, V_{cb}, \dots$
- Diodes numbered such that conduction sequence is:  $D_6/D_1, D_1/D_2, D_2/D_3, D_3/D_4, D_4/D_5, D_5/D_6, D_6/D_1, \dots$

CONSIDER WAVEFORMS + POWER FACTOR (e.g. for phase A)



# 6.334 Lecture Notes 4: 3φ Systems #2

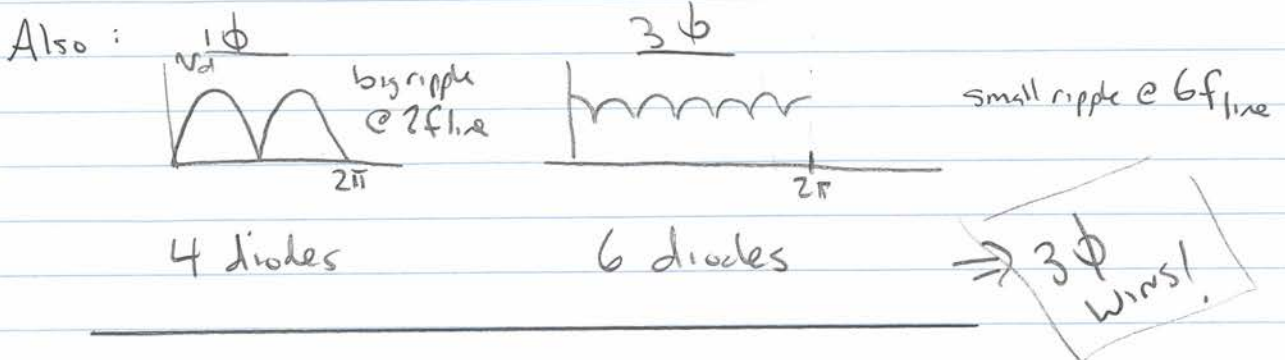
$$V_{a,rms} = \frac{V_s}{\sqrt{2}} \quad I_{a,rms} = \sqrt{\frac{2}{3}} I_d = \sqrt{\frac{2}{3}} I_d$$

$$\langle P \rangle = \frac{1}{\pi} \int_{\frac{\pi}{6}}^{\frac{5\pi}{6}} I_d V_s \sin(\varphi) d\varphi = \frac{V_s I_d}{\pi} \cdot \left[ \cos\left(\frac{\pi}{6}\right) - \cos\left(\frac{5\pi}{6}\right) \right]$$

$$\langle P \rangle = \frac{V_s I_d \sqrt{3}}{\pi}$$

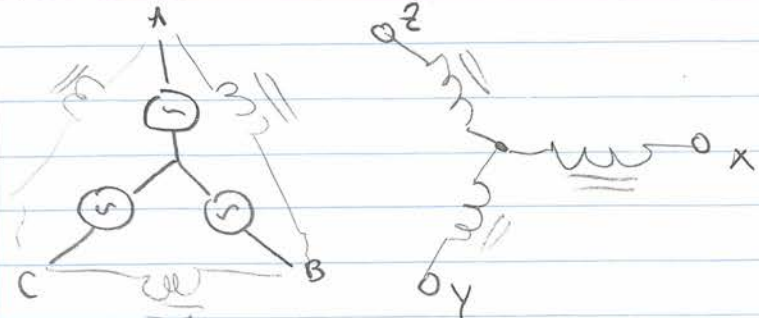
$$\Rightarrow K_p = \frac{\langle P \rangle}{V_{rms} I_{rms}} = \frac{V_s I_d \sqrt{3}}{\pi} \cdot \frac{\sqrt{2}}{V_s} \cdot \frac{\sqrt{3}}{I_d \sqrt{2}} = \frac{3}{\pi} \approx 0.96$$

This compares favorably to a single-phase bridge rect ( $K_p=0.91$ )  
 why better? : no triplen harmonics!



WE CAN DO EVEN BETTER (IN TERMS OF HARMONICS/P.F.)  
 WITH HIGHER-ORDER RECTIFIERS

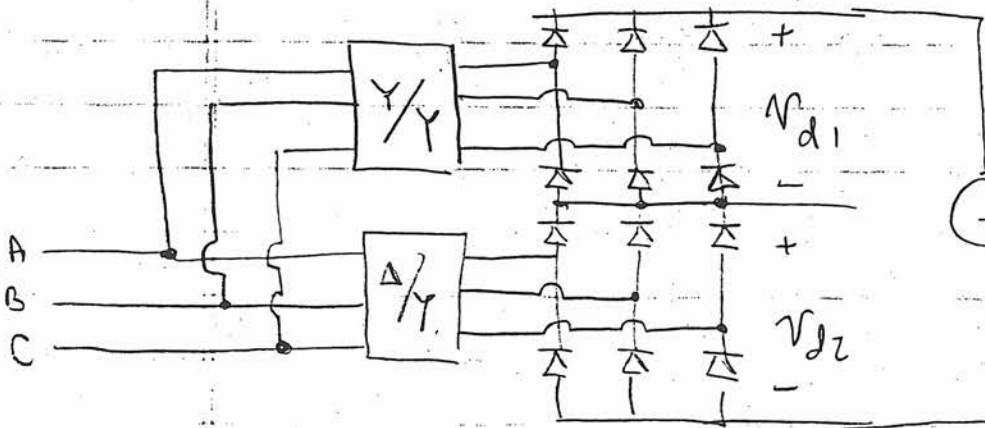
USING TRANSFORMERS, WE CAN GENERATE PHASE-SHIFTED VOLTAGE SETS



"Δ-Y" SET  $\Rightarrow$  Can create 30° phase shift ABC  $\rightarrow$  XYZ

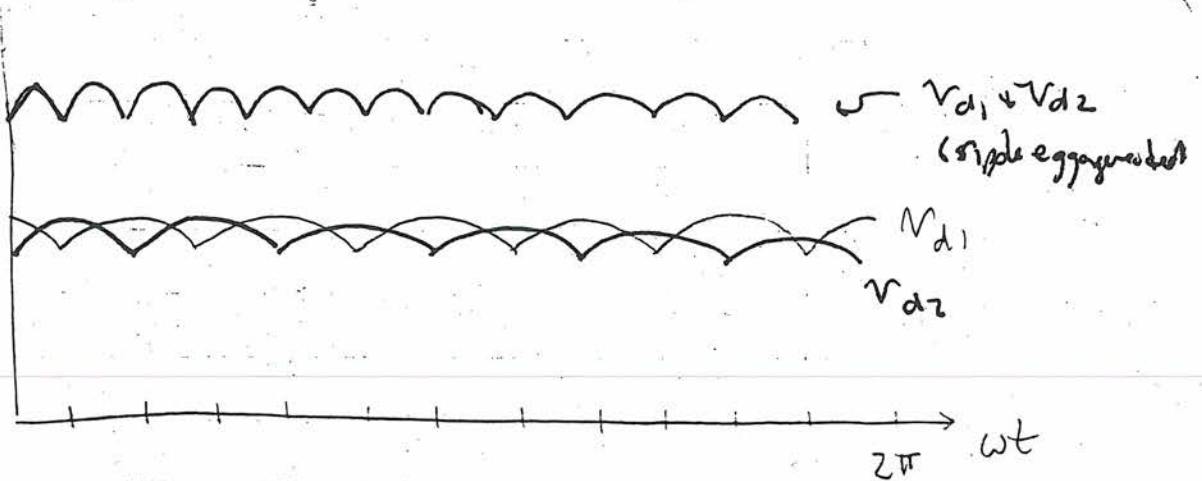
Power Electronics Notes - D. Perreault  
Higher-order Rectifiers

Suppose we use two phase-shifted transformer sets on series-stacked six-pulse bridges



The Y/Y, Δ/Y transformer sets generate equal voltage magnitudes, with a  $30^\circ$  phase difference between their  $3\phi$  outputs

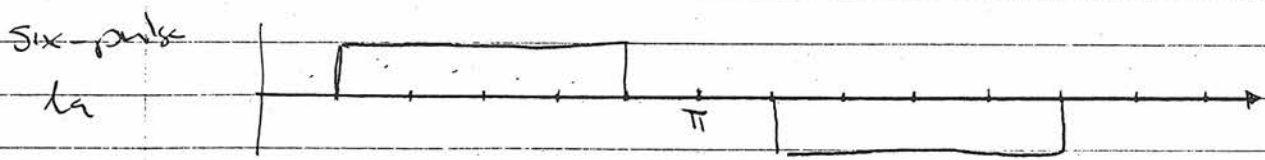
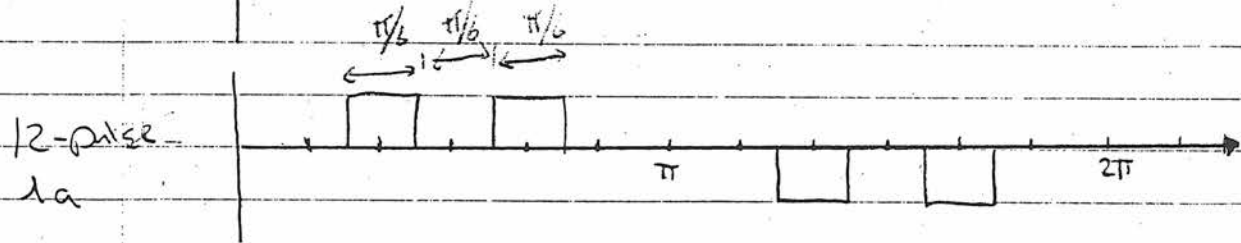
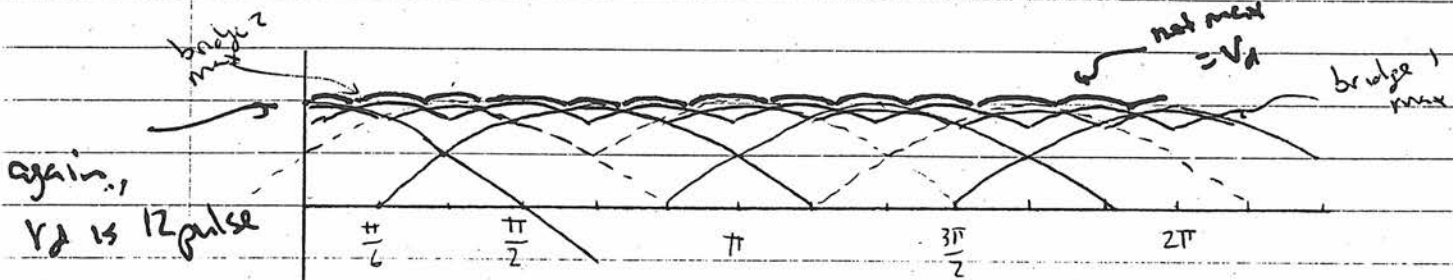
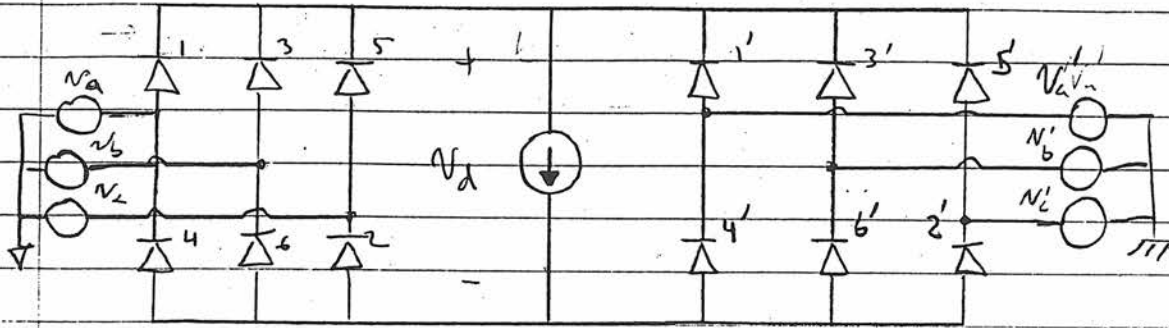
Since all  $V_d$  voltages are isolated, constant current in the bridges  $\rightarrow$  the two six pulse bridges act independently.  
 Since input waveforms shifted by  $30^\circ$  ( $\frac{T}{12}$ ) and output ripple is at  $6 \times$  input frequency ( $T_{\text{ripple}} = \frac{T}{6}$ )  
 Output ripple voltages are shifted by  $T_{\text{ripple}}/2$  ( $30^\circ$  fund, is  $180^\circ$  out)



We have a 12-pulse rectifier! (fundamental ripple waves cancel)  
 $\rightarrow$  smaller ripple magnitude } easier output filter  
 $\rightarrow$  higher ripple frequency }  
 $\rightarrow$  net input current + power factor also improves

# Power Electronics Notes - D. Perreault

consider parallel case (direct connection)



$$\text{12 pulse } I_{d,rms} = \sqrt{\frac{2\pi}{6} \cdot \frac{1}{2\pi} \cdot I_d^2} = \frac{I_d}{\sqrt{6}}$$

$$\text{6 pulse } I_{d,rms} = \sqrt{\frac{4\pi}{6} \cdot \frac{1}{2\pi} \cdot I_d^2} = \frac{I_d}{\sqrt{3}}$$

So 12 pulse,  $I_{rms} \downarrow$  by  $\sqrt{2}$ , but twice as many devices  
 → each device carries the full current for  $\frac{1}{2}$  the time!  
 (ohmic loss in devices, xformers, lines depend on RMS!)

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6.622 Power Electronics  
Spring 2023

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