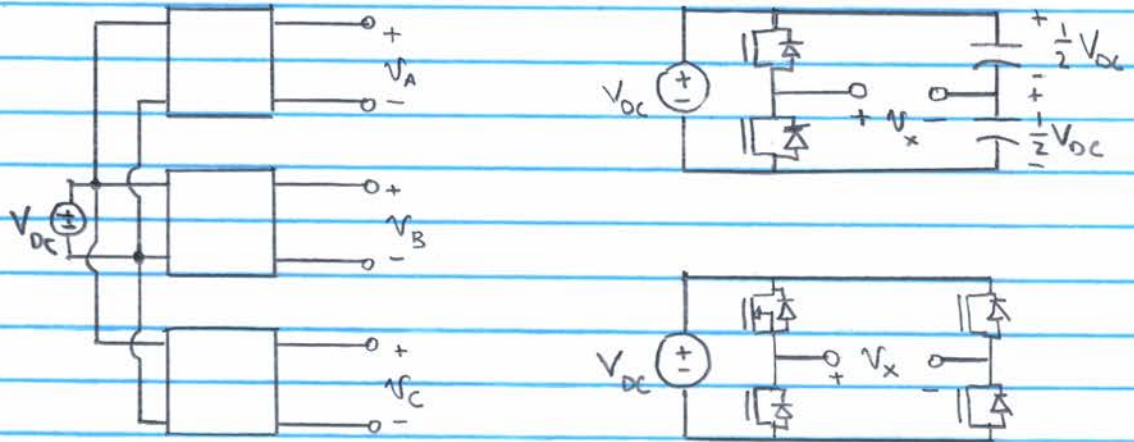


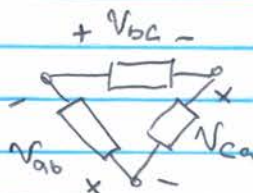
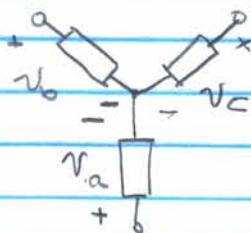
6.334 3-phase inverters

Consider implementation of an inverter for 3-phase using three single-phase inverters (e.g. full-bridge or half-bridge), one for each phase:



A half-bridge inverter requires only two devices and can synthesize a positive and a negative output $\{+\frac{1}{2}V_{dc}, -\frac{1}{2}V_{dc}\}$ but no zero state, while a full-bridge inverter can generate any of positive, negative and zero $\{+V_{dc}, -V_{dc}, 0\}$.

One might think that to realize a balanced 3-phase inverter would require as many as twelve devices to synthesize the desired output patterns. However, most 3-phase loads are connected in wye or delta, placing constraints on the instantaneous voltages that can be applied to each branch of the load

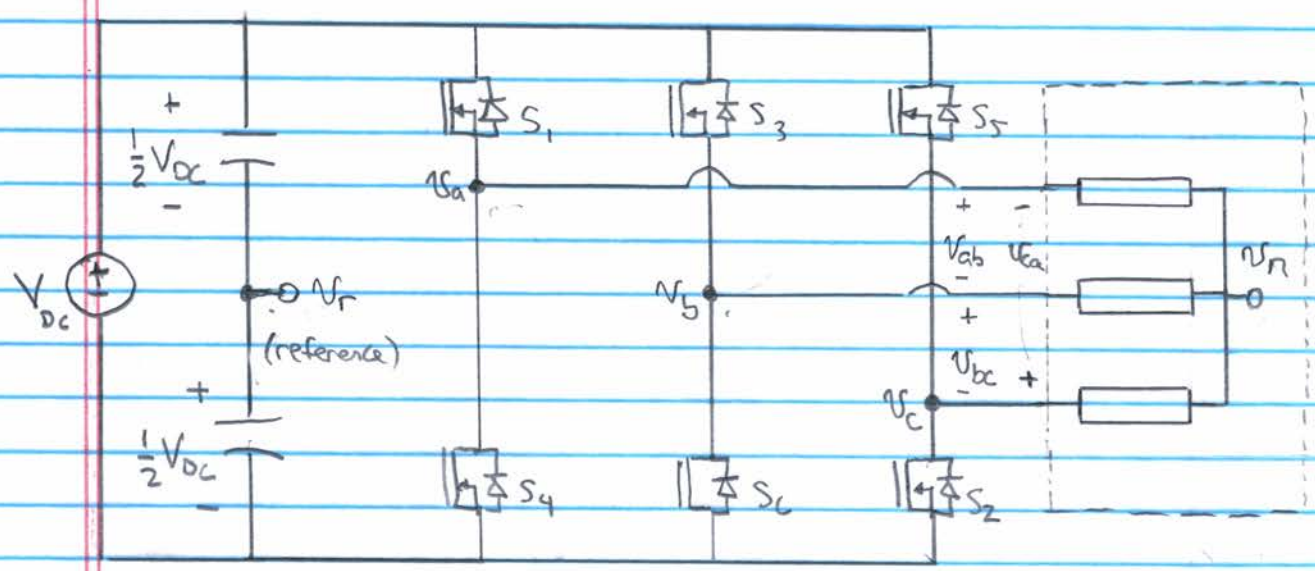


For the wye connection all the "negative" terminals of the inverter outputs are tied together, and for the delta connection the inverter output terminals are cascaded in a ring.

6.334 3-phase inverters

The load connections both limit the instantaneous voltages that may be synthesized with inverters comprising bridge legs fed from a single dc bus (without shorting the dc bus) and reduce the number of half-bridges needed to synthesize the allowed patterns.

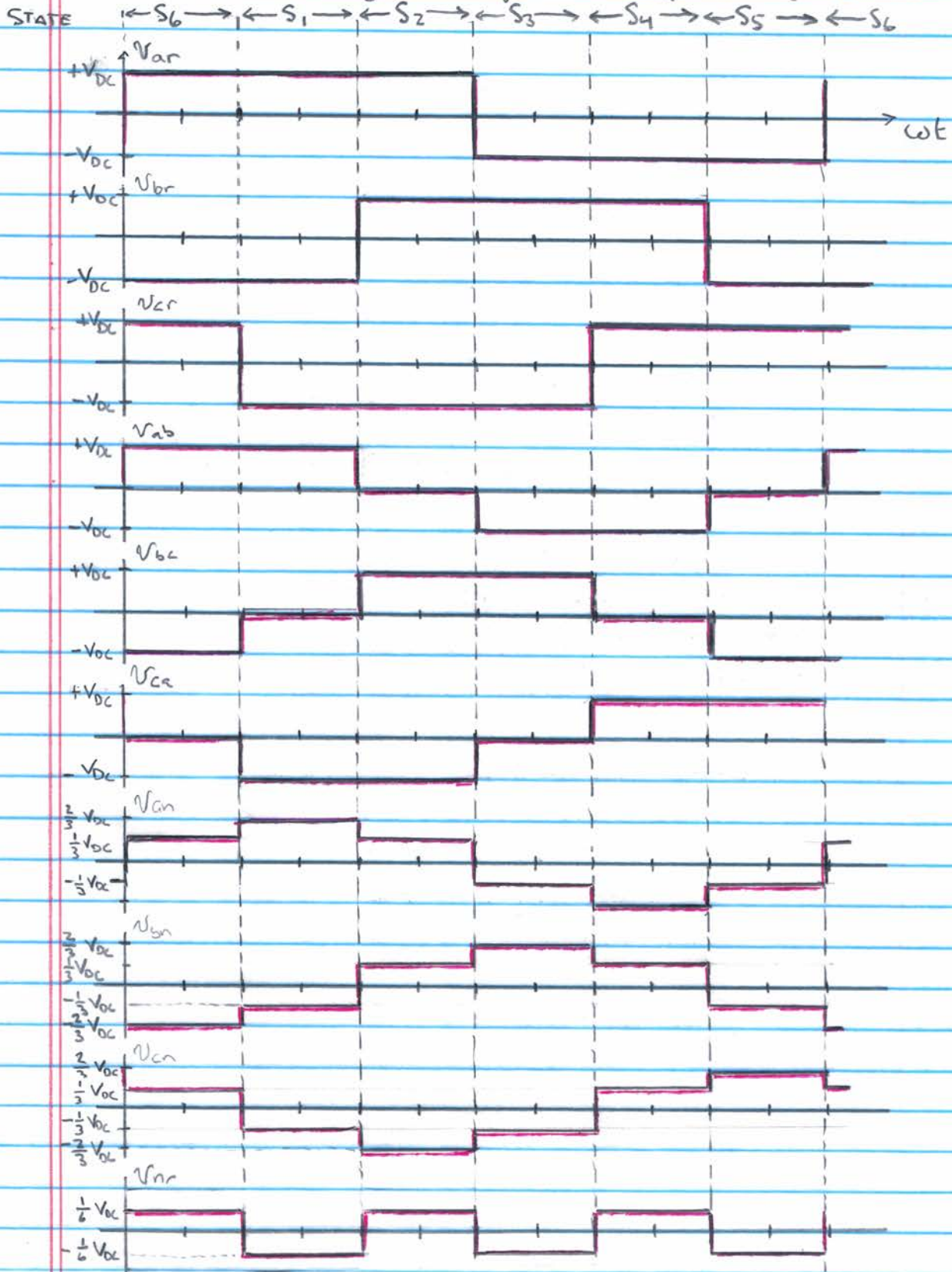
In particular considering "full-bridge" structures, half of the devices become redundant, and we can realize a 3-phase bridge inverter using only six switches (three half-bridge legs):



The 3-phase bridge comprises 3 half-bridge legs (one for each phase: a, b, c). The devices are often traditionally numbered as illustrated (conveying conduction order in "square wave" or "six step" operation, as is done for rectifiers). For symmetry and convenience, we utilize the midpoint of the dc bus as a voltage reference node. The connected load could be wye or delta, but we illustrate it as a wye connection with internal (unconnected) neutral point.

G.334 3-phase inverters

Lets consider inverter waveforms for the simple case where the 3 bridge legs each generate square waves spaced by $\frac{1}{3} T$:



6.334 3-phase inverters

This inverter operating mode - sometimes aptly called "six-step" mode - cycles sequentially through six of the 8 states defined above. The other two states are "zero" states which effectively short circuit the load terminals together. These provide means to apply zero-state voltages to the load when desired (e.g. for PWM control of the fundamental output voltage amplitudes)

Note that as the line-to-line voltages are formed as differences of identical waveforms shifted by $T/3$, they cannot contain any triple- n harmonics (in this mode or any other balanced 3-phase operating pattern).

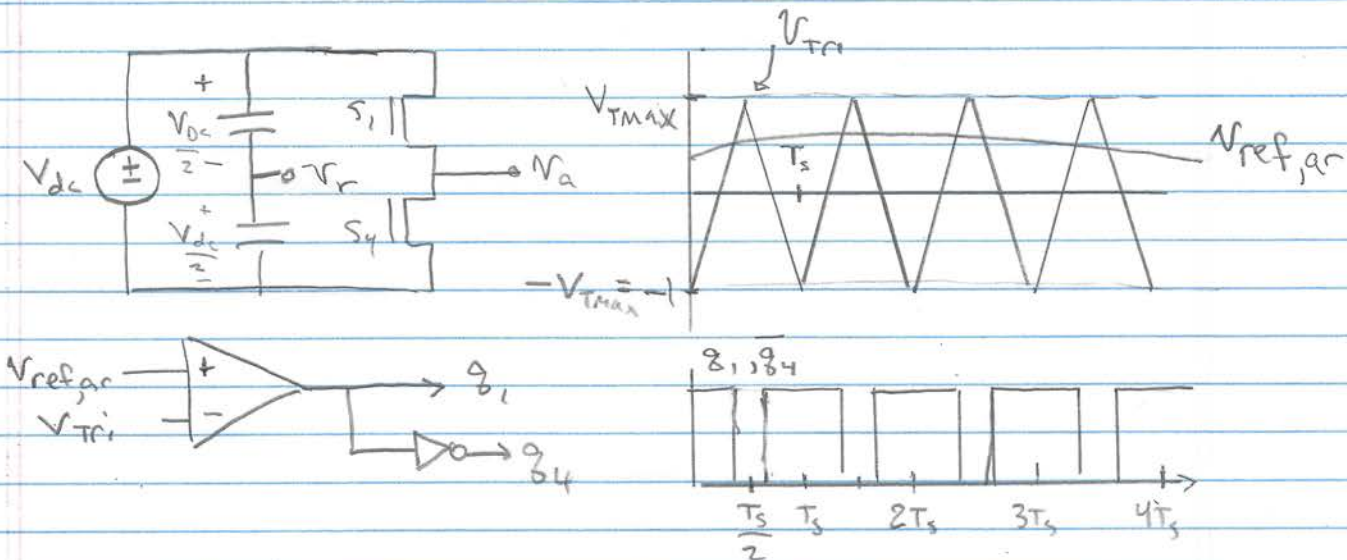
Likewise, for a 3-phase load network acting like 3 identical impedances connected to a (floating) neutral point, the neutral-point voltage becomes the average of the three phase voltages. If V_{ar}, V_{br}, V_{cr} are identical but shifted by $T/3$, they all have the exact same triple- n harmonic frequency content, and thus the neutral point "average" voltage V_{nr} will also have the exact same triple- n harmonic content. Consequently, the line-to-neutral voltages ($V_{xn} = V_{xr} - V_{nr}$, $x \in a, b, c$) will have no triple- n harmonic content. For the special case of six-step operation (V_{ar}, V_{br}, V_{cr} square waves), the neutral voltage V_{nr} becomes a square wave at a frequency $3 \times$ the fundamental.

The harmonic cancellation effect does limit the amplitude of the $2-2$ and $2-n$ fundamental voltages that can be synthesized to $<$ that achievable with square waves (e.g. as seen in the waveforms of V_{ab} or V_{an}).

If the neutral point (when available) is connected to the reference point, one gets very different operation. In that case we have three separate half bridges driving three independent phase windings, with no limitations on harmonic content, a slightly larger achievable fundamental, but all the synthesis limitations of half bridges.

6.334 3-phase inverters

Pulse-width modulation. There are multiple ways PWM might be realized. A simple one is to realize "sine Δ" PWM on each half-bridge.



over a switching period $\overline{V_{ar}} = \frac{V_{dc}}{2} \cdot \left(\frac{V_{ref,ar}}{V_{TMAX}} \right)$

So long as $|V_{ref}| < V_{TMAX}$

Suppose we wanted to synthesize a sine wave at a "line" frequency $\omega_l \ll \omega_s$ ($\omega_s = 2\pi/T_s$)

$\overline{V_{ar}} = V_m \sin(\omega_l t)$ then $V_{ref,ar} = \frac{V_m}{V_{dc}/2 (V_{TMAX})} \sin(\omega_l t)$

for $V_{TMAX} = 1$ $V_{ref,ar} = \frac{V_m}{V_{dc}/2} \cdot \sin(\omega_l t)$ modulation index m

This synthesizes a low-frequency sinusoidal component with a modulation index $m = \frac{V_m}{V_{dc}/2}$.

- for $\omega_s \gg \omega_l$ we get no low-frequency distortion in $\overline{V_{ar}}$ (or other output quantities) so long as modulation index $m \leq 1$
- For a balanced load (Δ or y connected with floating neutral) we cannot synthesize any triple-n components of ω_l

6.334 3-phase inverters

It is possible to synthesize outputs having a slightly larger amplitude than modulation index $m=1$ without low-frequency distortion.

For synthesizing a balanced 3-phase output set with modulation m , we get (local average over a cycle) switch duty ratios

$$d_1 = 1 - d_4 = \frac{1}{2} + \frac{m}{2} \sin(\omega_e t) \Rightarrow \bar{v}_{an} = m \frac{V_{dc}}{2} \sin(\omega_e t)$$

$$d_3 = 1 - d_6 = \frac{1}{2} + \frac{m}{2} \sin(\omega_e t - \frac{2\pi}{3}) \Rightarrow \bar{v}_{bn} = m \frac{V_{dc}}{2} \sin(\omega_e t - \frac{2\pi}{3})$$

$$d_5 = 1 - d_2 = \frac{1}{2} + \frac{m}{2} \sin(\omega_e t + \frac{2\pi}{3}) \Rightarrow \bar{v}_{cn} = m \frac{V_{dc}}{2} \sin(\omega_e t + \frac{2\pi}{3})$$

we need to keep all duty ratios between 0-1 to avoid low-frequency distortion.

If we add a signal at the 3rd harmonic of ω_e to each reference waveform, we can get to slightly larger values of m without saturating duty ratios at 0 or 1. The third harmonic that is synthesized is cancelled in the $l-l$ and $l-n$ waveforms (for a balanced load), so doesn't practically affect the output.

This technique is called "third harmonic injection" and lets us reach a modulation index m of up to $2/\sqrt{3} \approx 1.15$

To do this, set the references such that:

$$d_1 = 1 - d_4 = \frac{1}{2} + \frac{m}{2} \sin(\omega_e t) + \frac{m}{12} \sin(3\omega_e t)$$

$$d_3 = 1 - d_6 = \frac{1}{2} + \frac{m}{2} \sin(\omega_e t - \frac{2\pi}{3}) + \frac{m}{12} \sin(3\omega_e t)$$

$$d_5 = 1 - d_2 = \frac{1}{2} + \frac{m}{2} \sin(\omega_e t + \frac{2\pi}{3}) + \frac{m}{12} \sin(3\omega_e t)$$

add $\frac{V_{T,max}}{12} m \sin(3\omega_e t)$ to each reference waveform

MIT OpenCourseWare
<https://ocw.mit.edu>

6.622 Power Electronics
Spring 2023

For information about citing these materials or our Terms of Use, visit: <https://ocw.mit.edu/terms>