Lecture 28 - EMI Filtering 1

Need for input + Output Filters (start w/ Diff. Mode):

Input + output ripple/EMI Filtering is important to prevent interference with other circuitry (e.g., a power supply with an improperly designed EMI filter can easily interfere with radios, televisions, etc., in the vicinity)

Requirements are often imposed to prevent this:

    AC-line interfaced systems: FCC, VDE

    DC systems: MIL-STD-461, SAE J1113, CISPR

Ripple or EMI limits generally come in 2 flavors:

1. Time-domain voltage or current ripple limits

2. Frequency domain limitations

    • (i.e., amount of ripple energy detected within specified bandwidths over specified frequency ranges,)

    • e.g., FCC <u>conducted</u> limits use 9kHz "resolution bandwidth" from 450kHz - 30MHz

We will consider "conducted" EMI as a first step, as one will likely fail "radiated" EMI tests if conducted emissions are not controlled.

Start by considering "input filters" which prevent switching ripple being conducted back into the input line:

Consider a simple buck converter w/ capacitive input filter:
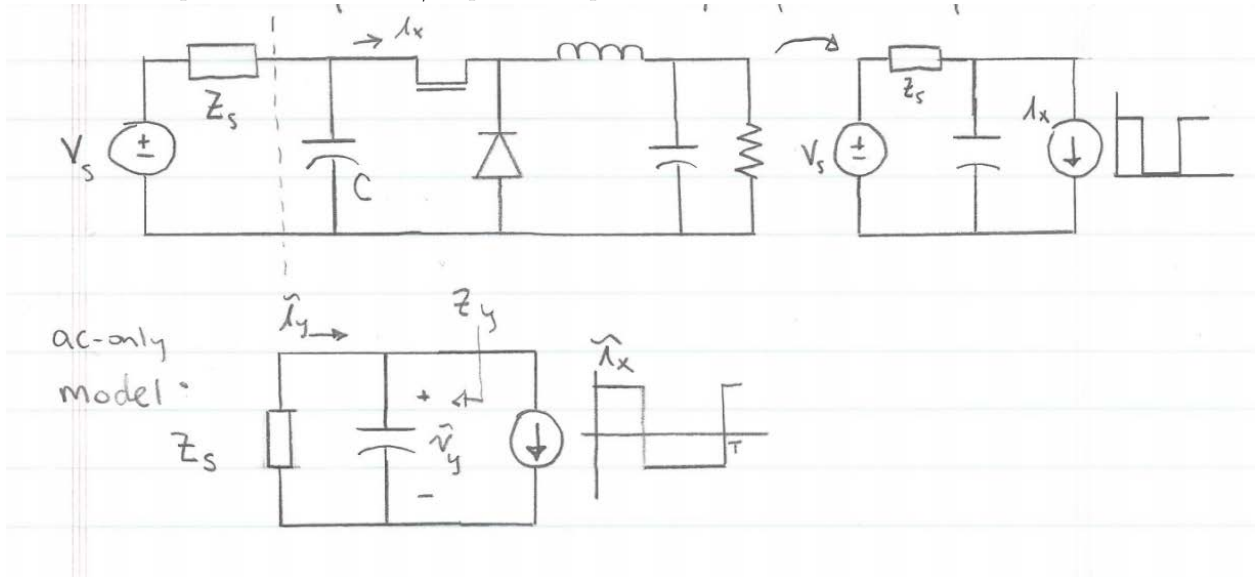
Input current $\tilde{i}_y$ and voltage $\tilde{v}_y$ will depend upon the source impedance Z_s (which we do not generally have control over).

• We won't get the same results for different conditions!

• $\lim_{|Z_s| \to 0} \Rightarrow \tilde{i}_y \to \tilde{i}_x, \tilde{v}_y \to 0$

- $\lim_{|Z_s| \to \infty} \Rightarrow \tilde{i}_y \to 0, |\tilde{v_y}(\omega)| \to |\frac{\tilde{I_x}(\omega)}{j\omega C}|$

To get repeatable results, we often do our measurements with a <u>Line Impedance Stabilization Network (LISN)</u> to get a repeatable, known condition for measurement:
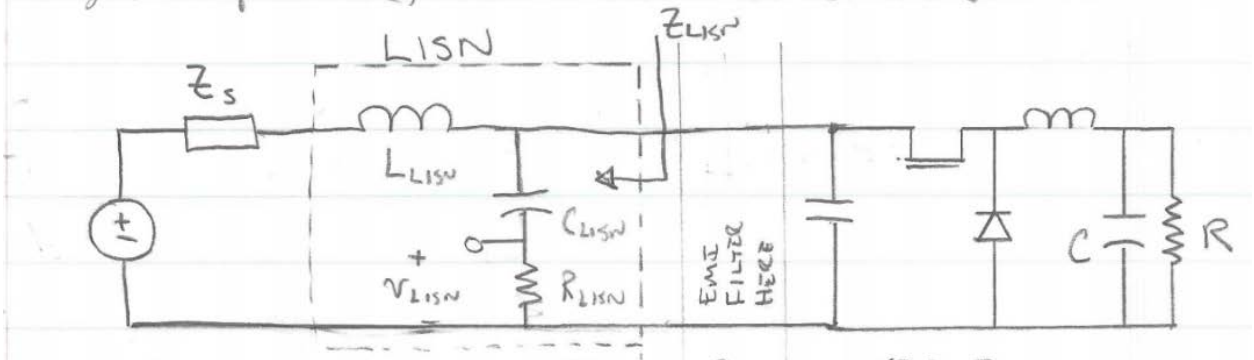

Typ $C_{LISN} = 1\mu F, L_{LISN} = 5\mu H, R_{LISN} = 50\Omega$
The LISN is used for EMI testing only:

- The LISN provides a known ripple frequency source impedance.

    - It is a short at DC (and @ low frequency) and passes power signals to the source.
    - At high frequency, $Z_{\text{LISN}} \to 50\Omega$.

- Often, the EMI specification is given in terms of frequency domain limits on V_{LISN} (across the 50Ω resistor), measured by a spectrum analyzer with a 50Ω input.

    - This corresponds to a limit of ripple content into the <u>known</u> LISN impedance.
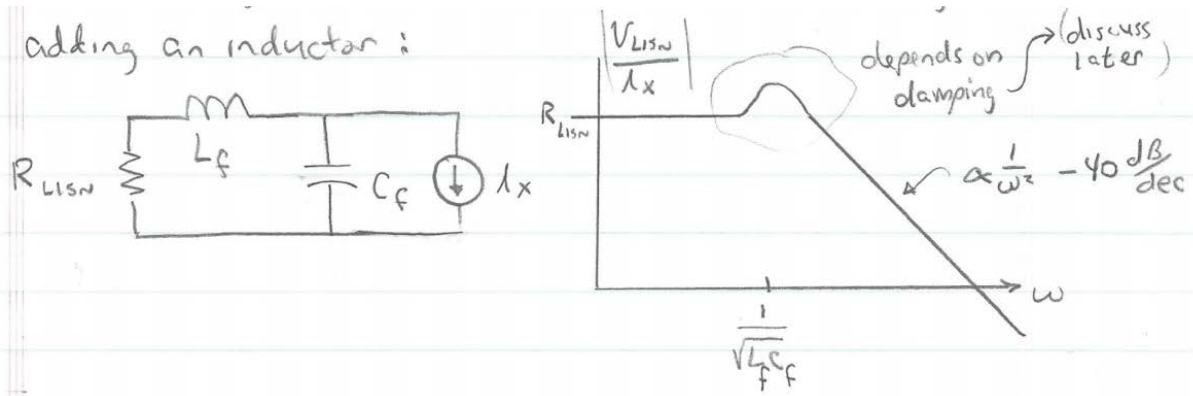

EMI Filter Design: (Simplify, assumign source impedance @ high frequency is 50Ω). For our buck converter:
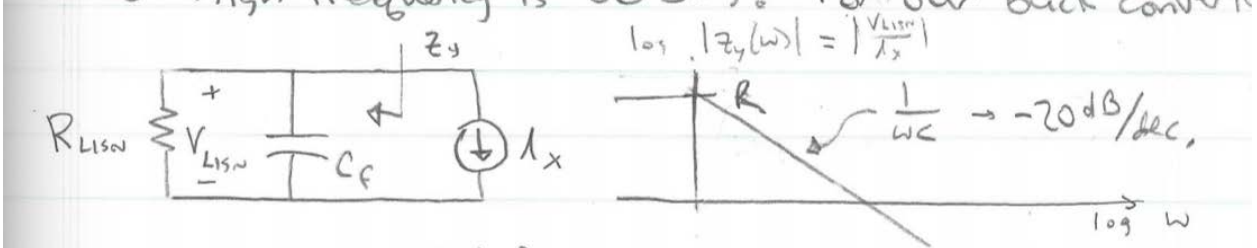
$$V_{LISN}(\omega) \approx \frac{I_x(\omega)}{j\omega C}$$

The capacitor to achieve typical ripple specifications may be impractically large! (e.g. for a typeical automotive application $|i_x|$ 10A, $|i_{R,LISN}| < 100\mu A$ is required!)

We can design a low-loss second order-filter by adding an inductor:

adding an inductor:

The mismatch between the growing impedance $j\omega L_f$ and the shrinking impedance $\frac{1}{j\omega c_f}$ gives attentuation $\propto \omega^2$!

Note: We must carefully consider component parasitics: e.g. consider a practical capacitor
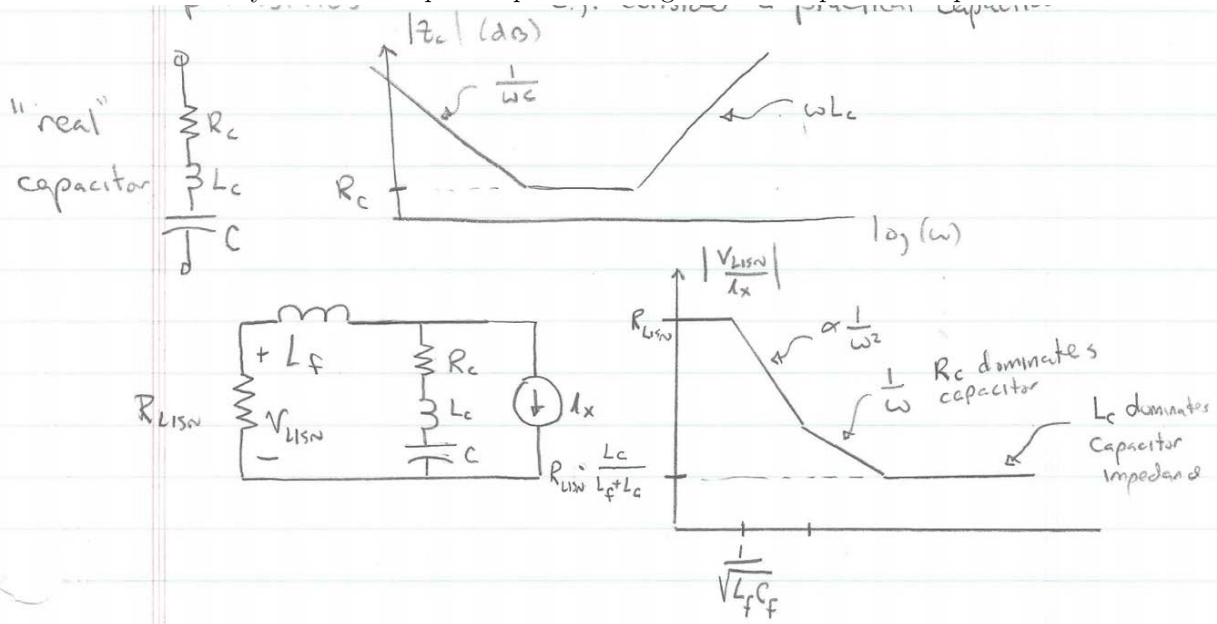
$\Rightarrow$ we need good components + careful layout, + still we must compensate for practical limits!
Note that in some cases it is the <u>parasitics</u> that determine component and filter specifications.
E.g. Electrolytic capacitor in an input filter

- "Low-frequency" ripple attenuation determined by R_{ESR}, not C

- Amount of capacitance (size) may be determined by RMS current ripple rating!

- We <u>often</u> parallel capacitors of different sizes & types

In addition to component parasitics, filter layout parasitics can be important.
Consider a high-attenuation filter: Even a very <u>small</u> parasitic capacitance can bypass the effect of the filter!
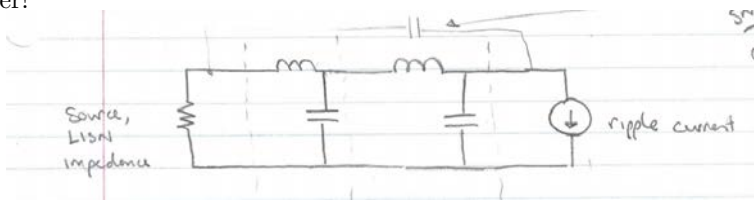
Such parasitic effects are often the source of seemingly anomalous behavior.

3

MIT OpenCourseWare

6.622 Power Electronics
Spring 2023