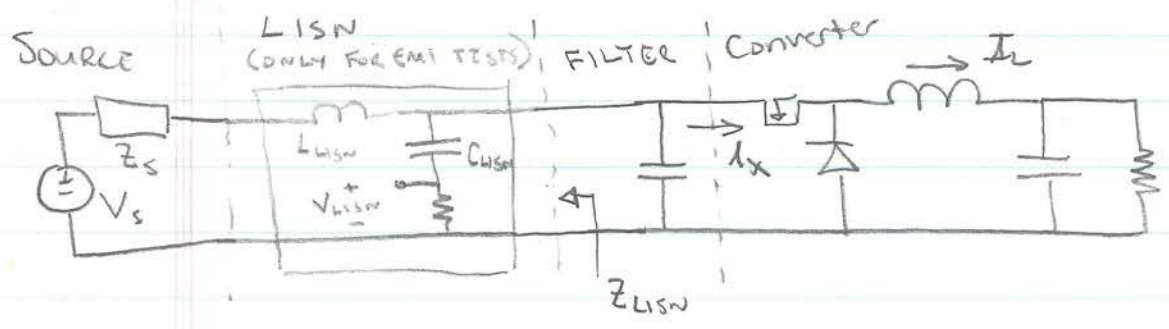


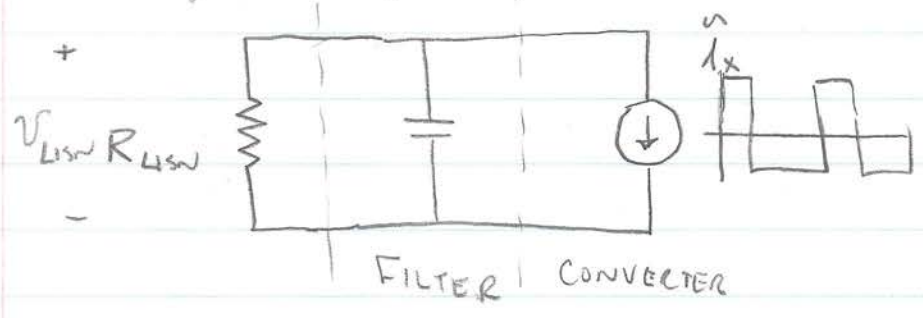
REVIEW:

- Filters are needed to achieve large attenuation of ripple
- To make measurements REPEATABLE a LISN is often specified for making EMI measurements:



- The LISN makes the ripple - frequency impedance looking back into the source known + repeatable
- Ripple measurements made @ the LISN "resistor": typ: a 50Ω input impedance of a spectrum analyzer

High-frequency ripple model

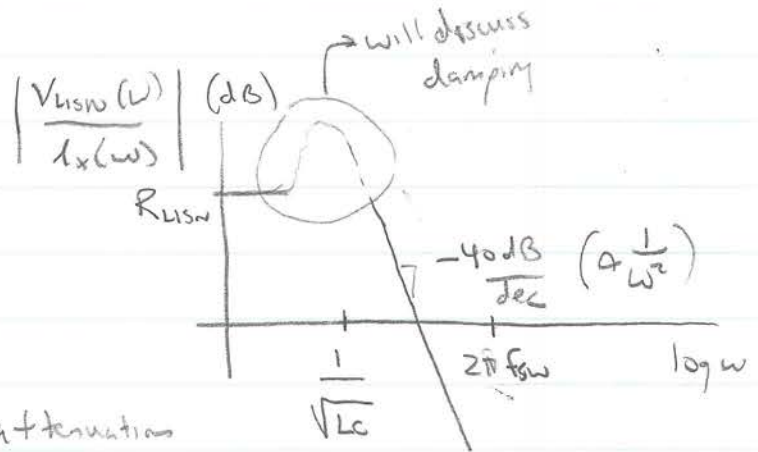
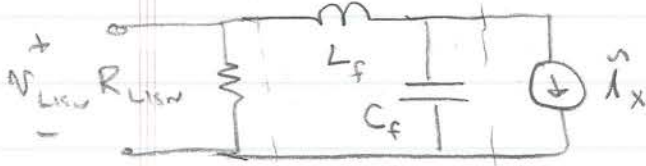


To achieve sufficient attenuation, a capacitor is often insufficient. A higher-order filter is needed. The filter should pass dc current with low loss, and accept dc voltage with low loss.

6.334 Lecture Notes

EMI Filters # 2

"L-section" Filter



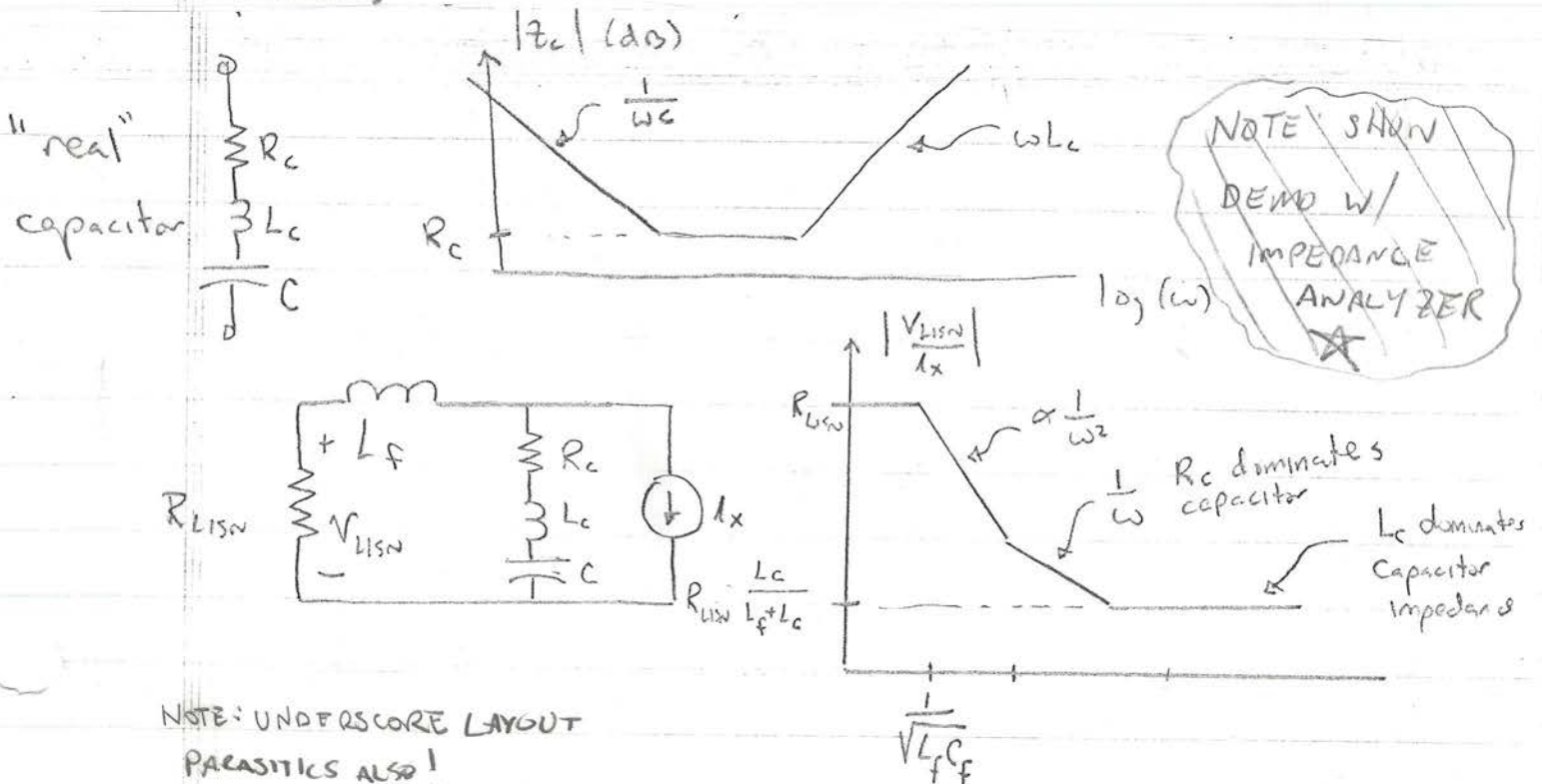
- We can get -40 dB/decade attenuation above the cutoff
- Blocks dc voltage well (capacitor), carries dc current well (inductor)
- Can cascade L sections if needed.

A second consideration is Filter + component parasitics

PARASITICS

+ Layout

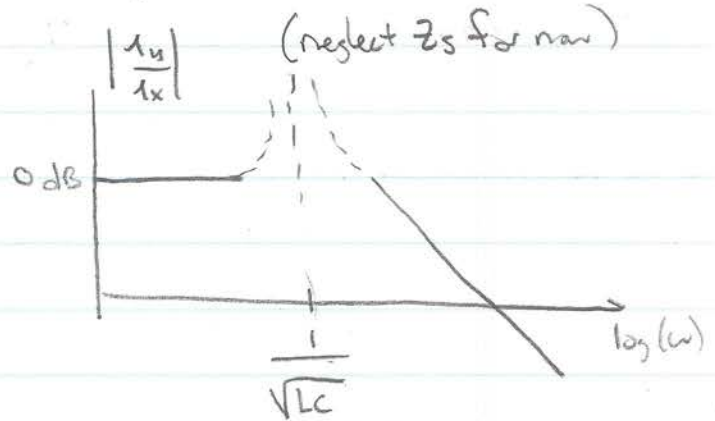
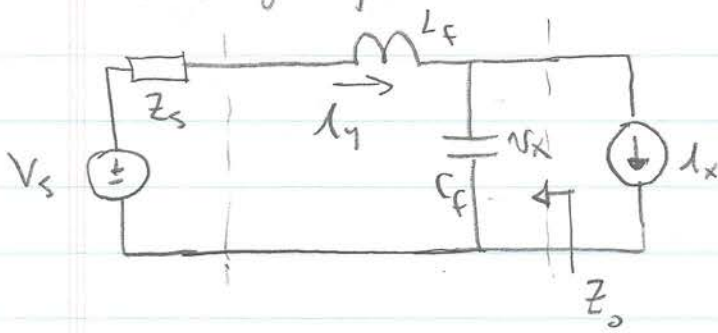
We must carefully consider component parasitics, as they limit H.F. performance!



NOTE: UNDERSCORE LAYOUT PARASITICS ALSO!

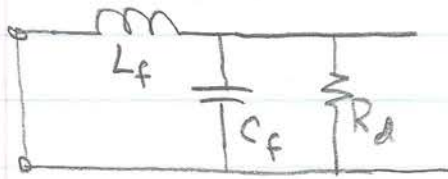
Component parasitics are an important limitation/consideration in design!

Another design issue is filter damping. (In practice, the LISN is not present, and we must have acceptable behavior across all frequency)



neglecting Z_s , setting $V_s \rightarrow 0$, the converter looks back into a parallel LC tank circuit. At resonance, this becomes a high impedance, + large resonant currents will flow! \Rightarrow must damp to be acceptable.

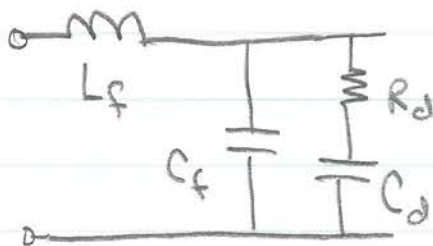
A natural idea is to add a resistor to damp the filter



If $R_d \approx \sqrt{\frac{L_f}{C_f}}$ we will be damped!

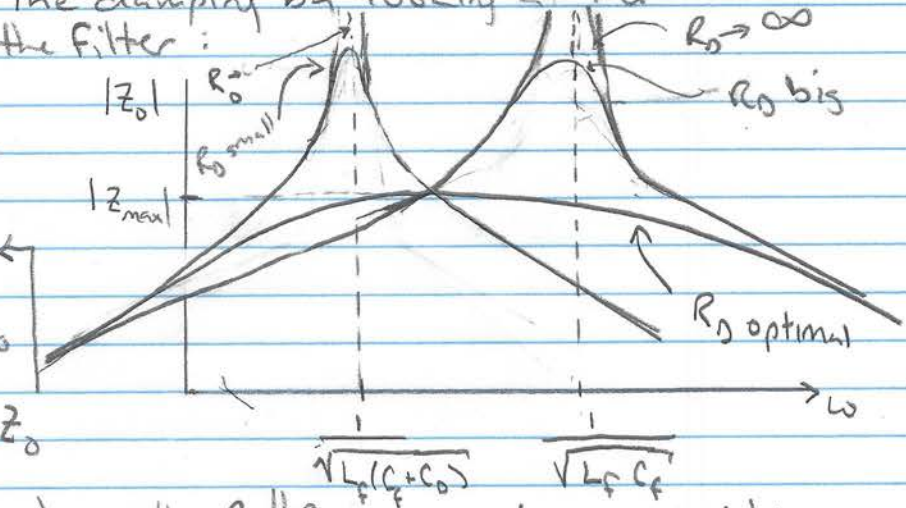
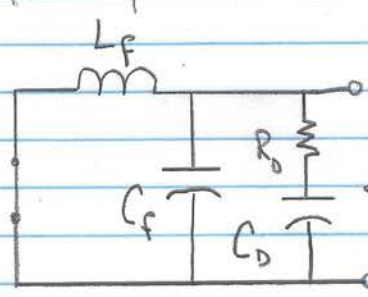


However, we could not accept the dc dissipation of R_d ! So, place it in series with a large dc blocking capacitor C_d , such that $R_d - C_d$ combination looks resistive @ $\omega = \frac{1}{\sqrt{L_f C_f}}$



- we want $C_d \gg C_f$ for effective performance
- C_d needs to be big, but unlike C_f
 - \rightarrow it only carries low current
 - \rightarrow it can have big parasitics (only capacitive @ low freq $\frac{1}{\sqrt{L_f C_f}}$)

Lets consider selection of the damping resistor R_D for a given choice of damping capacitor $C_D = n C_f$ (where $n \geq 1$)
 We can characterize the damping by looking at the output impedance of the filter:



Goldilocks:

- IF R_D too small, L_f resonates with $C_D || C_f$ and impedance peaks high
- IF R_D too big, L_f resonates with C_f and impedance peaks high
- IF R_D "just right", we minimize peaking for that value of $C_D = n C_f$

⇒ It can be shown (as in Erickson + Maksimovic) that the peak of the $|Z_0|$ curve for an optimal damping resistor that minimizes $\max\{|Z_0|\}$ is exactly the intersection point of the $|Z_0|$ curves for $R_D \rightarrow 0$ and $R_D \rightarrow \infty$.

* ⇒ choose a "goldilocks" value of R_D to achieve this minimum peaking. Can find by simulation sweep, or analytically as

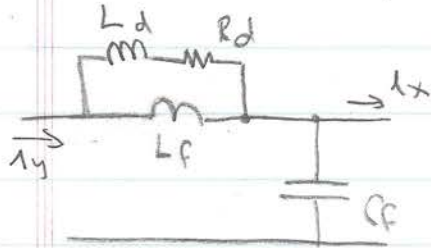
$$R_D \triangleq \sqrt{\frac{L_f}{C_f}} \quad n = \frac{C_D}{C_f} \quad R_D = R_{D,opt} \sqrt{\frac{(2+n)(4+3n)}{2n^2(4+n)}}$$

and we get a maximum output impedance: $|Z_{max}| = R_D \frac{\sqrt{2(2+n)}}{n}$

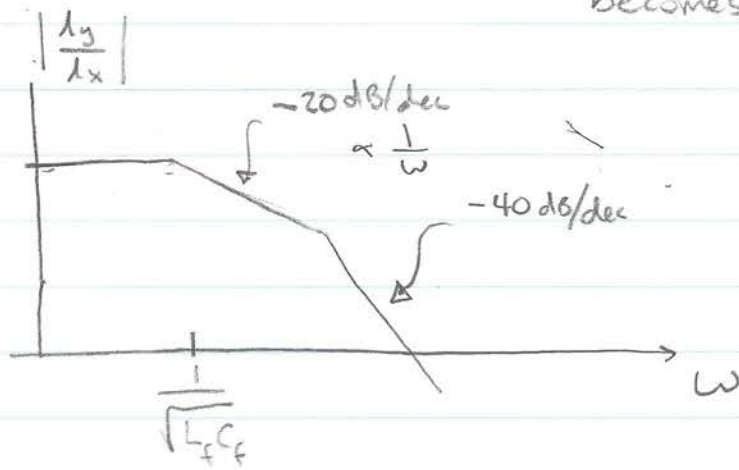
Note that in our filter design we choose $n = \frac{C_D}{C_f}$. the bigger the multiple n , the smaller we can make $|Z_{max}|$ (or equivalently the more damped we can make the filter for a given L_f, C_f).

we pay for a bigger n by requiring a higher C_D value (bigger damping capacitor)

We could also put the damping leg in parallel with L_f



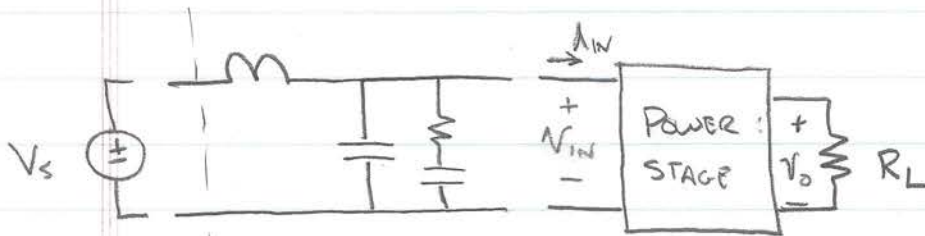
- $L_d - R_d$ branch looks resistive @ $\frac{1}{\sqrt{L_f C_f}}$
 $\Rightarrow L_d < L_f$
- @ high frequencies, series branch becomes $L_{eq} = L_d \parallel L_f < L_f$



- high-frequency attenuation not as good as undamped $L_f C_f$. Move cutoff down to compensate

- L_d can be small since it carries no dc current!

We must also consider the low-frequency interactions between the power converter + the filter!

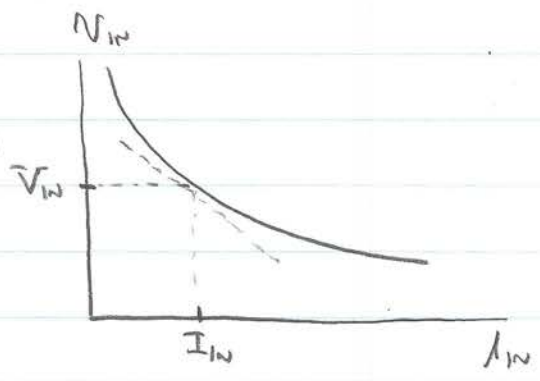


- To regulate the output to a specified voltage (controlled) the converter needs to draw a certain amount of power from the input, independent of voltage V_{in} !
 \Rightarrow It is a constant - power load!

6.334 Lecture Notes

EMI Filters # 2

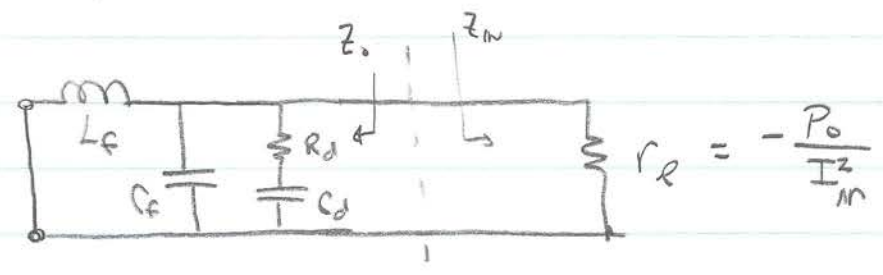
@ constant power P_o : $V_{IN} = \frac{P_o}{I_{IN}}$



for variations in input voltage, we get variations in current

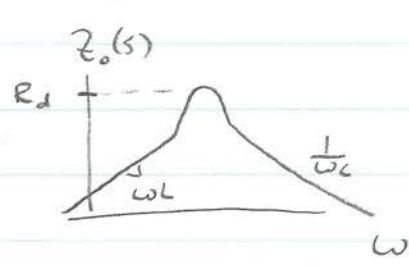
$$\left. \frac{\partial V_{IN}}{\partial I_{IN}} \right|_{op, pt} = - \frac{P_o}{I_{IN}^2} = r_l$$

So incrementally about the operating point, the converter looks like a negative resistance!



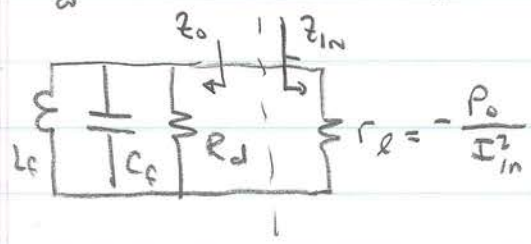
- we require that the system remain well damped despite the tendency of the negative resistance to undamp the system.

$$\text{Max}_{\omega} (Z_o(\omega)) \approx R_d$$



Simplified Approx: } neglects effects of C_d , but approximately true }

system approximation



$$\text{we need } R_d \parallel r_l > 0$$

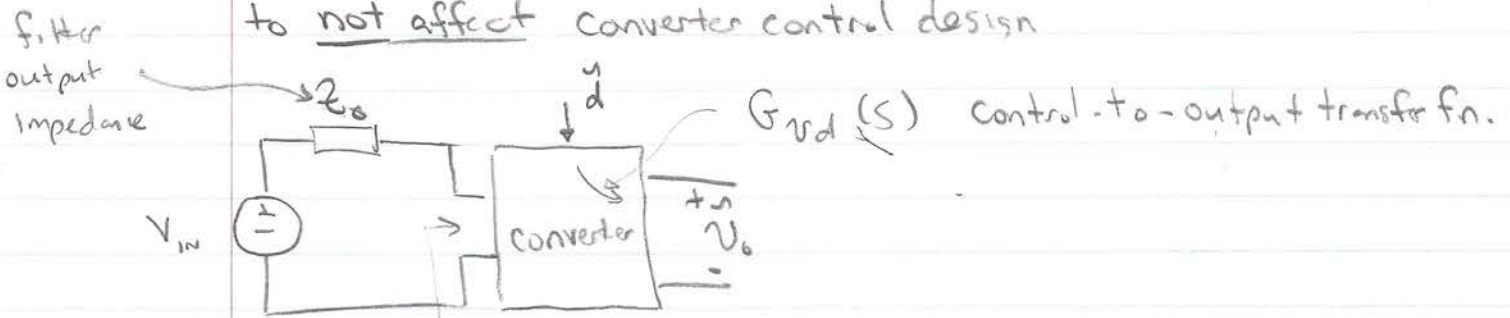
$$\frac{R_d r_l}{R_d + r_l} > 0 \text{ for } R_d < |r_l| \Rightarrow \text{make } R_d \ll \left| - \frac{P_o}{I_{IN}^2} \right|$$

for good damping!

In general: $\text{MAX}_{\omega} \{ Z_o \} \ll \left| - \frac{P_o}{I_{IN}^2} \right|$! } THERE ARE ALSO OTHER CONSIDERATIONS WE NEGLECT HERE ! }

⇒ Show Demo of Input filter oscillations

IF time permits: Show sufficient condition for input filter to not affect converter control design



Can prove (using Middlebrook extra element theorem)

$$G_{vd}(s) = \left(G_{vd}(s) \Big|_{z_o(s)=0} \right) \cdot \left[\frac{1 + \frac{z_o(s)}{z_n(s)}}{1 + \frac{z_o(s)}{z_0(s)}} \right]$$

Transfer fn. with no filter

- $z_o(s)$ filter output impedance
- $z_n(s)$ converter input impedance w/ perfect control of output $(= -P_o/I_{IN}^2)$
- $z_0(s)$ converter input impedance w/ CONSTANT duty ratio

Sufficient condition $|z_o(s)| \ll z_n(s)$ AND $z_0(s)$

(see Erickson + Maksimovic chapter for details)

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