1. Design Project Discussion
   (a) Where should you start? From the input? Or from the output?
   (b) What does Stage 1 do? What is the input? What is the output? What should nodes N1, N2, and N3 look like?
   (c) What does Stage 2 do? What is an XOR gate?
   (d) What does Stage 3 do? What is a buffer?